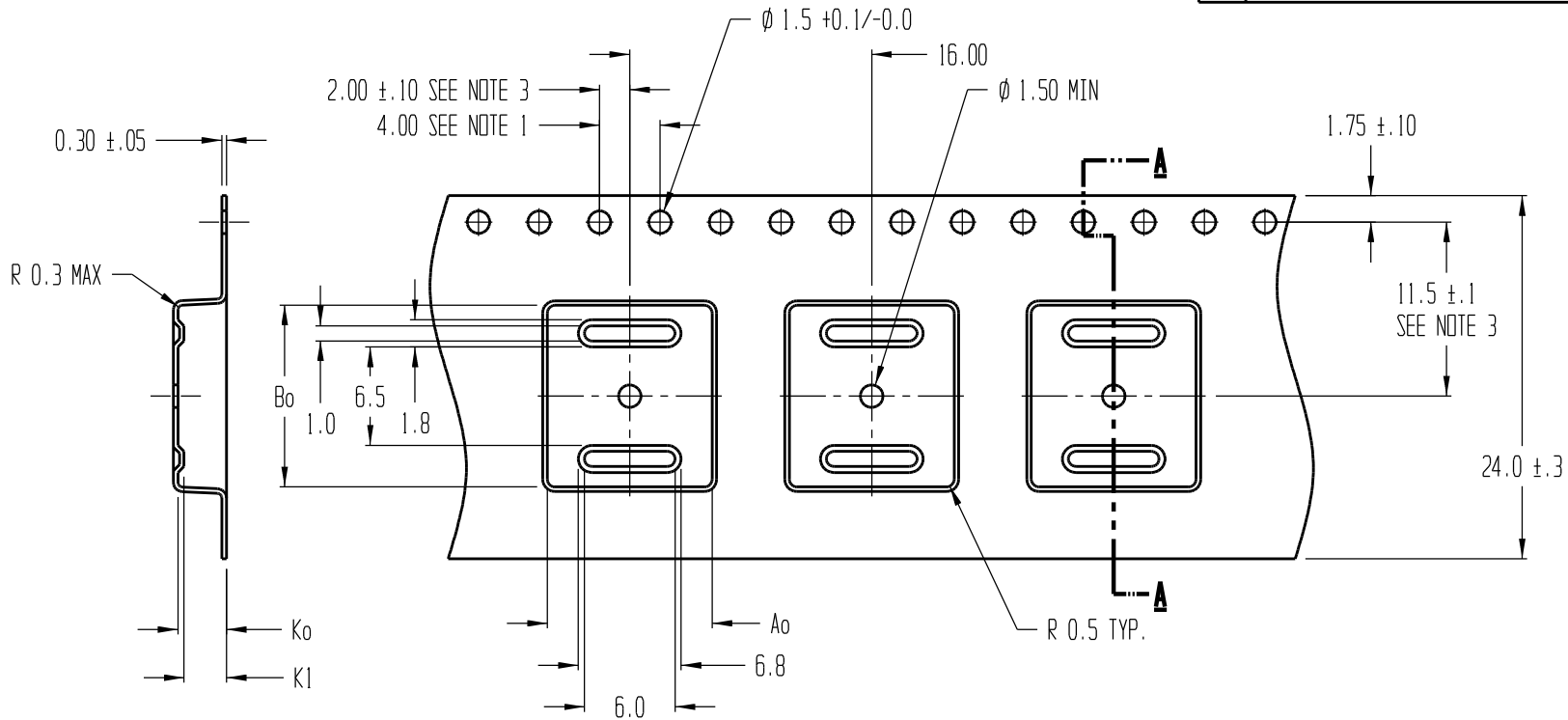


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
1	Drawing format change	3/18/02	BTH



**SECTION A - A**

$A_o = 10.90$   
 $B_o = 12.00$   
 $K_o = 3.20$   
 $K_1 = 2.80$

**NOTES:**

1. 10 SPROCKET HOLE PITCH CUMULATIVE TOLERANCE  $\pm 0.2$
2. CAMBER IN COMPLIANCE WITH EIA 481
3. POCKET POSITION RELATIVE TO SPROCKET HOLE MEASURED AS TRUE POSITION OF POCKET, NOT POCKET HOLE



<b>TITLE</b> ADVANTEK PART DRAWING NUMBER SD18LP-A 18 LEAD SOIC CARRIER TAPE			
<b>TOLERANCES - UNLESS NOTED</b> 1PL $\pm 0.2$ 2PL $\pm 0.10$		<b>MATERIAL</b> PS+C	
<b>ALL DIMENSIONS IN MILLIMETERS</b>		<b>DWG SIZE B</b>	
<b>DRAWN BY</b> RJB/BTH	<b>DATE</b> 3/15/95	<b>SCALE</b> 3:1	<b>SHEET 1 OF 1</b>
<b>REFERENCE NO.</b>		<b>DWG NO.</b> A0329-95-3	<b>REV</b> 1